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Convolution filter core

Program **Gama**

- Xilinx 7-series FPGA Support
- Modular design:
- Convolution core up to 5x5 size
  - User controlable computation precision
- Coloring module
- Conversion module
  - Bit depth normalization
  - RGB - YCrCb - Raw conversion

- Input video stream up to 1024p
- RAW, RGB support
- Up to 1200 Mpix/s
- Output conversion to
  - RAW, RGB, YCrCb
- Video data streaming via AMBA AXI4-Stream interface
- User control via AMBA AXI4-Lite
- Wide range of generic parameters for synthesis
- Ready asi IP CORE for EDK 14.7/Vivado
  - High level SW API
  - Validated in ISim and ModelSim
- Support : User manual, IP integration support
- Reference design for ZedBoard and microZed may be requested

Properties	
Documentation	Datasheet, VHDL codes
Format	NGC VHDL
Validation	Xilinx ISim ModelSim
Reference design	ZedBoard microZed
Support	IP integration Source codes
Software	drivers API examples

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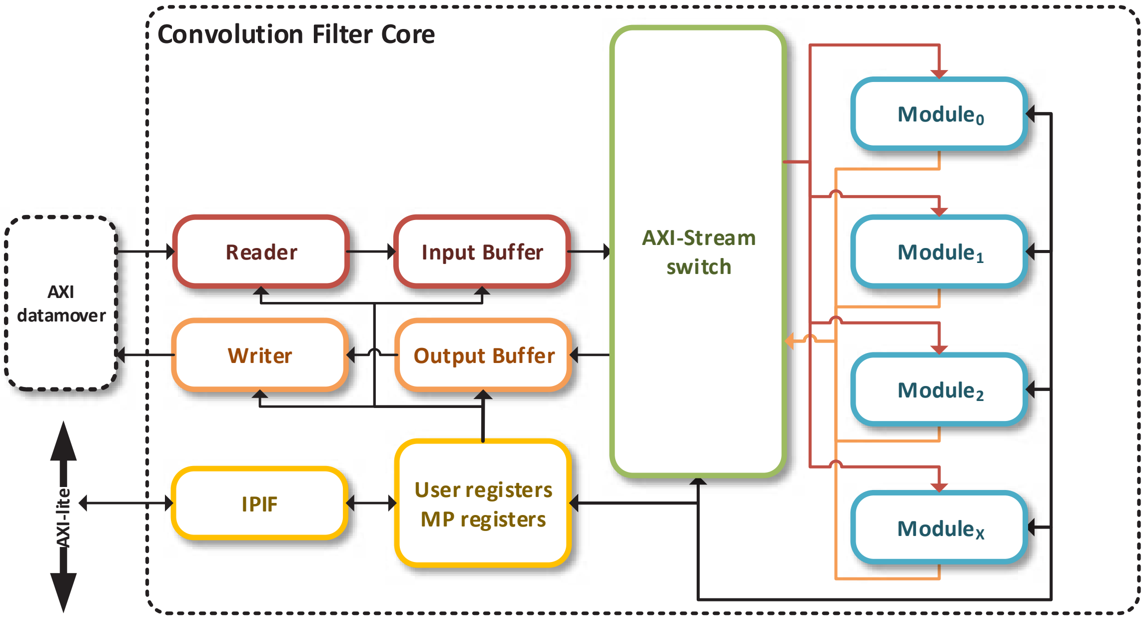

IP statistics

Device	Fmax (MHz)		LUT	FF	BRAM 36K	DSP48E1	Tools
	System	Processing					
Zynq 7000 ZC7020-1	150	212	2206	2558	7	28	Xilinx ISE 14.7

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